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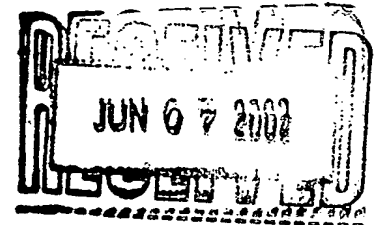
BOX DD

PATENT

Agent's Docket No. 11260-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of)
Simon J. SKIERSZKAN)
Serial No: 09/865,504) Art Unit: 2631
Filed: 29-May-2001) Examiner:
For: **TIMING CIRCUIT WITH DUAL PHASE LOCKED LOOPS**



May 27, 2002

Commissioner of Patent and Trademarks
Washington, D.C. 20231

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TRANSMITTAL LETTER

Please find enclosed for filing:

- Information Disclosure Statement – Form PTO 1449A
(with prior art documents attached)
- Certificate under 37CFR 1.97(e)

Respectfully submitted

BY: 

Richard J. Mitchell
Reg. No. 34,519
Agent of Record

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CERTIFICATE UNDER 37CFR 1.97(c)

Sir:

The applicants are submitting herewith an Information Disclosure Statement. The undersigned certifies that the information contained in the attached Information Disclosure Statement was cited in a Communication from a foreign Patent Office, in a counterpart foreign application, namely in an Office Action received from the German Patent Office dated March 12, 2002.

Abstract of DE 44 31415 has been provided. This patent was only cited to show the use of phase locked loops in general, and it is believe that the Abstract is sufficient for the Examiner's purposes.

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